L Number	Hits	Search Text	DB	Time stamp
Lituinbei	8491	(Isi or(logic adj circuit) or (integrated adj circuit) or MPU or	USPAT;	2003/11/26 08:53
	0-101	microprocessror) and ((peripheral or auxilliary) adj circuit)	US-PGPUB;	2003/11/20 00.03
		((periprieral of auxiliary) adjusticulty	EPO; JPO;	
			DERWENT	
	782	(//si or/logic adj sirguit) or (integrated adj sirguit) or MPLL or	USPAT;	2002/12/01 07:24
-	102	((Isi or(logic adj circuit) or (integrated adj circuit) or MPU or microprocessror) and ((peripheral or auxilliary) adj circuit)) and	US-PGPUB;	2003/12/01 07:34
		(one adj chip)	EPO; JPO;	
	9	((()) a di cianti	DERWENT	0000/44/00 00:54
-	9	(((((lsi or(logic adj circuit) or (integrated adj circuit) or MPU or	USPAT;	2003/11/26 08:54
		microprocessror) and ((peripheral or auxilliary) adj circuit)) and	US-PGPUB;	
		(one adj chip)) and debug) and @ad<20000710	EPO; JPO;	
,			DERWENT	
-	19	(((lsi or(logic adj circuit) or (integrated adj circuit) or MPU or	USPAT;	2003/11/26 14:37
		microprocessror) and ((peripheral or auxilliary) adj circuit)) and	US-PGPUB;	
i l		(one adj chip)) and debug	EPO; JPO;	
			DERWENT	
] -	19	(((lsi or(logic adj circuit) or (integrated adj circuit) or MPU or	USPAT;	2003/11/26 13:52
i l		microprocessor) and ((peripheral or auxilliary) adj circuit)) and	US-PGPUB;	
		(one adj chip)) and debug	EPO; JPO;	
			DERWENT	
-	1	09/900945	USPAT;	2003/11/26 13:52
			US-PGPUB;	
			EPO; JPO;	
			DERWENT	
-	123	(((lsi or(logic adj circuit) or (integrated adj circuit) or MPU or	USPAT;	2003/11/26 15:02
		microprocessror) and ((peripheral or auxilliary) adj circuit)))	US-PGPUB;	
1		and debug	EPO; JPO;	
1			DERWENT	
-	80	((((lsi or(logic adj circuit) or (integrated adj circuit) or MPU or	USPAT;	2003/11/26 14:38
		microprocessror) and ((peripheral or auxilliary) adj circuit)))	US-PGPUB;	
		and debug) and @ad<20000710	EPO; JPO;	
			DERWENT	
-	136	(((Isi or(logic adj circuit) or (integrated adj circuit) or ic or MPU	USPAT;	2003/11/26 15:04
		or microprocessror or processor) and ((peripheral or auxilliary)	US-PGPUB;	
		adj circuit))) and debug	EPO; JPO;	
			DERWENT	
-	90	((((lsi or(logic adj circuit) or (integrated adj circuit) or ic or MPU	USPAT;	2003/11/26 15:19
		or microprocessror or processor) and ((peripheral or auxilliary)	US-PGPUB;	
		adj circuit))) and debug) and @ad<20000710	EPO; JPO;	
]			DERWENT	
-	347	((Isi or(logic adj circuit) or (integrated adj circuit) or MPU or	USPAT:	2003/12/01 07:35
		microprocessror) and ((peripheral or auxilliary) adj circuit)) and	US-PGPUB;	
		(memory or DRAM or array)and (debug or lbist or test).ab.	EPO; JPO;	
		,,,,,	DERWENT	
-	241	(((lsi or(logic adj circuit) or (integrated adj circuit) or MPU or	USPAT:	2003/12/01 07:36
		microprocessror) and ((peripheral or auxilliary) adj circuit)) and	US-PGPUB;	=355=.0
		(memory or DRAM or array)and (debug or lbist or test).ab.)	EPO: JPO:	
		and @ad<20000710	DERWENT	
		and @ad -200007 TO	PELLAAFIAI	<u></u>